

ELGE-1A Fundamentals of Electronics

Credits: Theory-03

Total Lectures: 45h

Course Learning Objectives

- The paper equips the learners about basic circuit knowledge to analyze electric circuits using network theorems.
- Understand diode and its applications in clipping and clamping circuits, Rectifiers and design regulated power supply using Zener diodes.
- To be able to plot the current voltage characteristics of Diode, Transistors and its different biasing conditions
- Usage of semiconductor devices in designing the circuits.

Course Learning Outcomes

At the end of this course, Students will be able to

- CO1 Study basic circuit concepts in a systematic manner suitable for analysis and design and further analyze the electric circuit using network theorems.
- CO2 To understand the different types of semiconductor devices and their characteristics
- CO3 Illustrate about working of transistors, transistor-based amplifiers and its biasing.
- CO4 Explain the concepts of feedback and oscillations and construct feedback amplifiers

L-T-P: 3-0-1

Syllabus Content

Unit 1: Basic Resistive Circuit

12 Lectures

Ohm's Law, resistors in series and parallel combinations. DC voltage sources: ideal and non-ideal cases; DC current sources: ideal and non-ideal cases; Introduction to Kirchhoff's current law, Kirchhoff's voltage law, voltage divider circuit, current divider circuit; source transformations– voltage source to current source and current source to voltage source, basic problems. Resistive circuits: Thevenin's theorem, Norton theorem, Superposition theorem, Maximum power transfer theorem.

Unit 2: PN-junction diode and its applications

12 Lectures

PN junction, Unbiased PN junction, Forward and Reversed biased condition, IV-characteristics of PN junction diode, types of diodes – Zener diode, photo diode, LED.

Diode circuits and power supplies. Half and full wave rectifiers, Bridge rectifier (qualitative comparison), Regulated power supply using Zener diode, Basic Clipper and Clamper circuits using diodes.

Unit 3: Bipolar Junction Transistors (BJT) and Biasing

11 Lectures

NPN Transistor and basic transistor action, Definition of α , β and γ and their interrelations, leakage currents, Modes of operation, Input and output characteristics of CB, CE and CC Configurations. Transistor biasing, thermal runaway, stability and stability factor, Fixed bias without and with R_E , collector to base bias, voltage divider bias and emitter bias ($+V_{CC}$ and $-V_{EE}$ bias), circuit diagrams and their working.

Unit 4: BJT applications

10 Lectures

BJT amplifier (CE), dc and ac load line analysis, Operating point, Concept of feedback, negative and positive feedback, advantages and disadvantages of negative feedback, voltage (series and shunt), current (series and shunt) feedback amplifiers, gain, input and output impedances. Positive feedback and Barkhausen criteria for oscillations.

Suggested Books:

1. R. L. Boylestad & Louis Nashlesky (2007), Electronic Devices & Circuit Theory, Pearson Education.
2. David A. Bell (2008), Electronic Devices and Circuits, Oxford University Press.
3. [B L Theraja](#) and AK Theraja, [A Textbook Of Electrical Technology - Vol I](#).
4. Donald A. Neamen, Electronic Circuit Analysis and Design, Tata McGraw Hill (2002)

**Fundamentals of Electronic Lab
(Hardware and Circuit Simulation Software)**

Credits: 01

Lectures: 30h

Course Learning Outcomes

At the end of this course, Students will be able to

- CO1 Verify the network theorems and operation of typical electrical circuits.
- CO2 Study various stages of a zener diode based regulated power supply.
- CO3 Understand various biasing concepts, BJT based amplifiers.

Syllabus Content

1. Study and operation of digital multi-meter, function generator, regulated power supply, CRO, etc.
2. Verification of KVL and KCL.
3. Verification of Superposition theorem.
4. Verification of Thevenin's, Norton's Theorem
5. Verification of Maximum power transfer theorem.
6. To plot the IV-characteristics of a ordinary and Zener diode and LED
7. Study of Half wave and Full Wave Rectifiers
8. Study of Fixed Bias, Voltage divider bias Feedback configuration for transistors.
9. Study of transistor amplifier circuit.

Indicative Course Teaching-Learning Processes and Assessment Methods are listed in section 7.3 and 7.4 respectively along with Table 1 on Suggestive Learning and Evaluation Strategies.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.